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No. 5852389 Kumar, "Direct QAM Modulator" ... a high speed reference clock providing in an input signal having a series of pulses at a frequency of the ...

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Mobile Handset DesignLine | Design an all-digital modulator with ...

Quantization is needed to ensure that the high-speed reference clock runs at a reasonable ... To accommodate the bandwidth requirement in the QAM modulator, ...
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... therefore the frequency of the high-speed reference clock is 3.2 GHz. ... The QAM modulator consists of one QAM symbols generator whose symbol rate is ...

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The all-digital transmitter consists of one QAM modulator and counter using the high speed reference clock. However, because ...

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10/796416 entitled APPARATUS FOR DIGITAL VECTOR QAM MODULATOR and application Ser. ... a high speed reference clock providing in an input signal having a ...

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High speed data service via satellite modem termination system and ...

60/410870, filed September 13, 2002 and entitled "HIGH SPEED DATA SERVICE VIA ...

An example of this QAM modulator 312 is the BCM3033 Modulator chip from ...

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- ☐ 1. Method and apparatus for digital vector QAM modulator
Harron, Gerald / Kumar, Surinder, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Apr 2005
patno: US20050089117
...used. The output frequency range coverage...up to the high speed reference clock frequency...high Power QAM Modulator"[0010] U...Direct QAM Modulator" (SUMMARY...0013) a high speed reference clock providing...the desired output frequency; [0014] two...
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- ☐ 2. Method and apparatus for fractional RF signal synthesis
Harron, Gerald / Kumar, Surinder (VCom Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Aug 2006
patno: US7084676
...accumulator. The output frequency range coverage...up to the high speed reference clock frequency...a desired output frequency comprising: [0011] a high speed reference clock providing...the desired output frequency; [0012] programmable...
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- ☐ 3. Method and apparatus for fractional RF signal synthesis
Harron, Gerald / Kumar, Surinder, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Apr 2005
patno: US20050083085
...accumulator. The output frequency range coverage...up to the high speed reference clock frequency...a desired output frequency comprising: [0011] a high speed reference clock providing...the desired output frequency; [0012] programmable...
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- ☐ 4. Apparatus for fractional RF signal synthesis with phase modulation
Harron, Gerald / Kumar, Surinder, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Apr 2005
patno: US20050091295
...described is that the output frequency 108 synthesis range...block multiple

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Parent Data

10796416, filed 03/10/2004

Claims Priority from Provisional Application 60513985, filed 10/27/2003

Child Data

No Child Data

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Application Number: 10/796416

AssignmentsExaminer Number: 80488 / TORRES, JUAN

Filing or 371(c) Date: 03/10/2004 eDan

Group Art Unit: 2611

IFW Madras

Effective Date: 03/10/2004

Class/Subclass:

375/298.000

Application Received: 03/10/2004

Lost Case: NO

Waiting for Response

Pat. Num./Pub. Num: /20050089117

Interference Number:

Desc,

Issue Date: 00/00/0000

Unmatched Petition: NO

Mail Non Final

Date of Abandonment: 00/00/0000

L&R Code; Secrecy Code:1

Attorney Docket Number: 85195-502 ADB

Third Level Review: NO

Secrecy Order: NO

Status: 41 /NON FINAL ACTION MAILED

Status Date: 11/27/2007

Confirmation Number: 9044

Oral Hearing: NO

Title of Invention: METHOD AND APPARATUS FOR DIGITAL VECTOR QAM
MODULATOR

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<input type="button" value="Update"/>					

Inventor Information for 10/796416

Inventor Name	City	State/Country
HARRON, GERALD	MARTENSVILLE	CANADA
KUMAR, SURINDER	VICTORIA	CANADA

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Last Name = HARRON

First Name = GERALD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07511069	5065107	150	04/19/1990	PHASE-LOCKER LOOP BANDWIDTH SWITCHING DEMODULATOR FOR SUPPRESSED CARRIER SIGNALS	HARRON, GERALD
10671873	Not Issued	161	09/29/2003	Method and apparatus for reducing peak to average power ratio in QAM multi-channel blocks	HARRON, GERALD
10671879	Not Issued	161	09/29/2003	Method and apparatus for reducing the power consumption of the power amplifier used in a QAM modulator	HARRON, GERALD
10796415	Not Issued	71	03/10/2004	Apparatus for fractional RF signal synthesis with phase modulation	HARRON, GERALD
10796416	Not Issued	41	03/10/2004	Method and apparatus for digital vector QAM modulator	HARRON, GERALD
10796417	7084676	150	03/10/2004	METHOD AND APPARATUS FOR FRACTIONAL RF SIGNAL SYNTHESIS	HARRON, GERALD
10796418	7012477	150	03/10/2004	MODULATION USING DISCRETE AMPLITUDE ADJUSTMENT AND DUAL DIGITAL DELAY LINES	HARRON, GERALD
10796419	6982607	150	03/10/2004	AMPLITUDE AND PHASE MODULATION USING DUAL DIGITAL DELAY VECTORS	HARRON, GERALD
60414393	Not Issued	159	09/30/2002	Method and apparatus for reducing the power consumption of the power amplifier used in a QAM modulator	HARRON, GERALD
60414394	Not Issued	159	09/30/2002	Method and apparatus for reducing peak to average power ratio in QAM multi-channel blocks	HARRON, GERALD
60511677	Not Issued	159	10/17/2003	Method and apparatus for fractional RF signal synthesis	HARRON, GERALD
60513984	Not	159	10/27/2003	Method and apparatus for	HARRON, GERALD

	Issued			fractional RF signal synthesis with phase modulation	
60513985	Not Issued	159	10/27/2003	Method and apparatus for digital vector QAM modulator	HARRON, GERALD
60525117	Not Issued	159	11/28/2003	Amplitude & phase modulation using dual digital delay vectors	HARRON, GERALD
60525118	Not Issued	159	11/28/2003	Modulation using discrete amplitude adjustment & dual digital delay lines	HARRON, GERALD
10015885	Not Issued	161	12/17/2001	Printed circuit board radio frequency filter apparatus	HARRON, GERALD D.
09209295	6064285	150	12/11/1998	PRINTED CIRCUIT BOARD HELICAL RESONATOR AND FILTER APPARATUS	HARRON, GERALD D.

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Your Search was:

Last Name = KUMAR

First Name = SURINDER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06216407	Not Issued	161	12/15/1980	LOW-CHOLESTEROL SAUSAGE-LINK ANALOG AND PROCESS THEREFOR	KUMAR, SURINDER
06288091	4376134	250	07/28/1981	LOW-CHOLESTEROL SAUSAGE ANALOG AND PROCESS THEREFOR	KUMAR, SURINDER
06618225	4613509	150	06/07/1984	PROCESS FOR PRODUCING CENTER-FILLED FOOD PRODUCTS	KUMAR, SURINDER
06841256	4808422	150	03/19/1986	CENTER-FILLED FOOD PRODUCTS	KUMAR, SURINDER
07467379	5187803	250	01/18/1990	REGENERATIVE RF BI-DIRECTIONAL AMPLIFIER SYSTEM	KUMAR, SURINDER
07496332	5025452	250	03/20/1990	FULL-DUPLEX, SUB-BAND SPREAD SPECTRUM COMMUNICATIONS SYSTEM	KUMAR, SURINDER
07511069	5065107	150	04/19/1990	PHASE-LOCKER LOOP BANDWIDTH SWITCHING DEMODULATOR FOR SUPPRESSED CARRIER SIGNALS	KUMAR, SURINDER
08008618	Not Issued	161	01/22/1993	REGENERATIVE RF BIDIRECTIONAL AMPLIFIER SYSTEM	KUMAR, SURINDER
08837395	5852389	150	04/17/1997	DIRECT QAM MODULATOR	KUMAR, SURINDER
08843970	6111477	150	04/17/1997	MICROWAVE PHASE SHIFTER INCLUDING A REFLECTIVE PHASE SHIFT STAGE AND A FREQUENCY MULTIPLICATION STAGE	KUMAR, SURINDER
08877772	5825257	150	06/17/1997	GMSK MODULATOR FORMED OF PLL TO WHICH CONTINUOUS PHASE MODULATED SIGNAL IS APPLIED	KUMAR, SURINDER
10671872	7248664	150	09/29/2003	TIMESLICED DISCRETE-TIME	KUMAR,

				PHASE LOCKED LOOP	SURINDER
10671873	Not Issued	161	09/29/2003	Method and apparatus for reducing peak to average power ratio in QAM multi-channel blocks	KUMAR, SURINDER
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60414392	Not Issued	159	09/30/2002	Time sliced discrete-time PLL apparatus and method	KUMAR, SURINDER
60414393	Not Issued	159	09/30/2002	Method and apparatus for reducing the power consumption of the power amplifier used in a QAM modulator	KUMAR, SURINDER
60414394	Not Issued	159	09/30/2002	Method and apparatus for reducing peak to average power ratio in QAM multi-channel blocks	KUMAR, SURINDER
60511677	Not Issued	159	10/17/2003	Method and apparatus for fractional RF signal synthesis	KUMAR, SURINDER
60513984	Not Issued	159	10/27/2003	Method and apparatus for fractional RF signal synthesis with phase modulation	KUMAR, SURINDER
60513985	Not Issued	159	10/27/2003	Method and apparatus for digital vector QAM modulator	KUMAR, SURINDER
60525117	Not Issued	159	11/28/2003	Amplitude & phase modulation using dual digital delay vectors	KUMAR, SURINDER
60525118	Not Issued	159	11/28/2003	Modulation using discrete amplitude adjustment & dual digital delay lines	KUMAR, SURINDER
60894238	Not Issued	20	03/12/2007	METHOD FOR REDUCING INTER MODULATION DISTORTION PRODUCTS OF A COMBINED CARRIER WAVE USING PHASE ALIGNMENT	KUMAR, SURINDER

				OF THE CARRIER COMPONENTS	
10015885	Not Issued	161	12/17/2001	Printed circuit board radio frequency filter apparatus	KUMAR, SURINDER G.
09209295	6064285	150	12/11/1998	PRINTED CIRCUIT BOARD HELICAL RESONATOR AND FILTER APPARATUS	KUMAR, SURINDER G.

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